PCIE Protocol Interview Question on PHY Layer

1. How to remove the first and last element in a queue using a single statement (or) method? Mainly related to score boarding. (SV, UVM question)?
2. How do you come to know equalization has been completed in each phase successfully? (protocol)
3. Draw a wavefrom on the pipe signal to indicate the speed change that happened on the RC?
4. What is Tx equalization & Rx equalization? (protocol)
5. What is the difference between hot react and detect? (protocol)
6. What is the conditions and rules for state transistor form LO to hot react in the LTSSM? (protocol).
7. How do you transistor from P2 to Lo?(protocol)
8. How do you verify the equalization has been failed? (protocol)
9. What is the difference between package & include (SV)?
10. How do you come to know that Invalid preact values (or) invalid request has been sent for the equalization? (protocol).
11. LTSSM state transition from Lo state to recovery conditions?(protocol).